

PTQ/SB/DBa (08-09)

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**SUPPLEMENTAL  
INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet	1	of	1
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**Complete If Known**

<b>Application Number</b>	10/647,595
<b>Filing Date</b>	August 25, 2003
<b>First Named Inventor</b>	S. Brandon Keller
<b>Art Unit</b>	2409
<b>Examiner Name</b>	Not Yet Assigned
<b>Attorney Docket Number</b>	100111227-1

## U.S. PATENT DOCUMENTS

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**FOREIGN PATENT DOCUMENTS**

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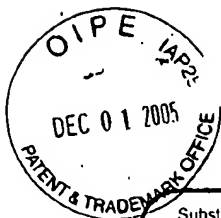
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PTO/SB/08a (08-03)

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Sheet 1

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Application Number	10/647,595
Filing Date	August 25, 2003
First Named Inventor	S. Brandon Keller
Art Unit	2825
Examiner Name	Not Yet Assigned
Attorney Docket Number	100111227-1

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
8		US-5,831,869	11-03-1998	Ellis, et al.	
		US-6,028,991	02-22-2000	Akashi	
		US-6,230,299	05-08-2001	McSherry, et al.	
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		US-			
		US-			
		US-			

**FOREIGN PATENT DOCUMENTS**

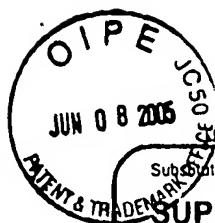
Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				

Examiner Signature		Date Considered	8/24/06
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Sheet 1 of 1

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First Named Inventor	S. Brandon Keller
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Attorney Docket Number	100111227-1

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		Number - Kind Code <sup>2</sup> (if known)			
		US-2002/0144219	10-03-2002	Zachariah, et al.	
		US- 2003/0200519	10-23-2003	Argyres	
		US- 2003/0208721	11-06-2003	Regnier	
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		US- 6,378,123	04-23-2002	Dupontoup	
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<b>Application Number</b>	<b>10/647,595</b>
<b>Filing Date</b>	<b>August 25, 2003</b>
<b>First Named Inventor</b>	<b>S. Brandon Keller</b>
<b>Art Unit</b>	
<b>Examiner Name</b>	
<b>Attorney Docket Number</b>	<b>100111227-1</b>

Sheet	1	of	1
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[illegible][illegible]

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LIST OF PATENTS AND PUBLICATIONS FOR  
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STATEMENT

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ATTY. DOCKET NO.

100111227-1

APPLICATION NO.

10/647,595

CONFIRMATION NO.

APPLICANT

S. Brandon Keller, et al.

FILING DATE

Aug. 25, 2003

GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A			
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	1L				
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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

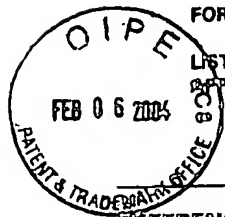
8	1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
8	1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
8	1S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003

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	2L				
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	2P				

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

8	2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
8	2R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003
8	2S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,892, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

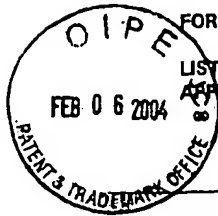
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## PATENT APPLICATION

Sheet 3 of 6



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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

2	3Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
2	3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655516US; Entitled: Method And Program Product For Determining Worst Case Currents In A Digital Integrated Circuit Through Worst-Case Superposition Of Partial Currents; Docket No.: 200300123-1; Filed January 30, 2004
2	3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655564US; Docket: 200308901-1; Entitled: Systems And Methods For Re-Using Circuit Design Analysis Results; Filed January 30, 2004

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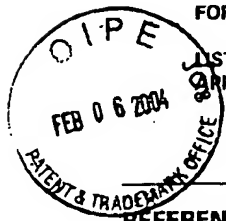
8	4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655581US; Docket No.: 200308899-1; Entitled: System And Method For Determining Detail Of Analysis In A Circuit Design; Filed January 30, 2004
8	4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655595US; Docket No.: 200308898-1; Entitled: Systems And Methods That Identify Equivalent Instantiation-Specific Configuration Information For Analysis Tools; Filed January 30, 2004
8	4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H. & STEVENS, Scott Alan; U.S. Patent Application filed under EV210655520US; Entitled: System And Method To Limit Analyzed Current Flow In A Circuit Design; Filed January 30, 2004

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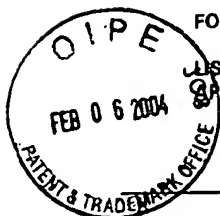
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81	5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655533US; Docket: 200310172-1; Entitled: System And Method For Processing Configuration Information; Filed January 30, 2004
82	5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004
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6Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655547US; Docket: 200310171-1; Entitled: System And Method For Determining Control Signal Combinations For Use During Simulation Of A Stage Of A Circuit Design; Filed January 30, 2004
6R	
6S	

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